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Robin E. Barnum (Depositor's name)

(Signature)

February 27, 2006 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/702,320	10/31/2000	Laurence R. Simar, Jr.	TI-30559	9784

TITLE OF INVENTION: VERY LONG INSTRUCTION WORD MICROPROCESSOR WITH EXECUTION PACKET SPANNING TWO OR MORE FETCH PACKETS WITH PRE-DISPATCH INSTRUCTION SELECTION FROM TWO LATCHES ACCORDING TO INSTRUCTION BIT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	04/12/2006
EXAMINER	ART UNIT	CLASS-SUBCLASS			
HUISMAN, DAVID J	2183	712-205000			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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2. For printing on the patent front page, list

(1) the names of up to 3 registered patent attorneys or agents OR, alternatively,

(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 Robert D. Marshall, Jr.

2 W. James Brady, III

3 Frederick J. Telecky, Jr.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.111. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Texas Instruments Incorporated

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Dallas, TX USA

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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☒ Issue Fee☐ Publication Fee (No small entity discount permitted)☐ Advance Order - # of Copies \_\_\_\_\_

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☒ The Director is hereby authorized by charge the required fee(s), or credit any overpayment, to Deposit Account Number 20-0668 (enclose an extra copy of this form).

5. Change in Entry Status (from status indicated above)

☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

The Director of the USPTO is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above.

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Authorized Signature

Robert D. Marshall, Jr.Date February 27, 2006Typed or printed name Robert D. Marshall, Jr.Registration No. 28,527

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450,

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### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-30559

Laurence R. Simar, Jr., et al.

Art Unit: 2183

Serial No.: 09/702,320

Examiner: David J. Huisman


Filed: October 31, 2000

Conf. No.: 9784

For: Very Long Instruction Word Microprocessor With Execution Packet Spanning Two or  
 More Fetch Packets With Pre-Dispatch Instruction Selection From Two Latches According to Instruction  
 Bit (amended title)

#### CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent  
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 Robin E. Bamum

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<b>NAME OF INVENTOR(S):</b> Laurence R. Simar, Jr., et al.	<b>RECEIPT DATE &amp; SERIAL NO.:</b> <b>Serial No.: 09/702,320</b> <b>Filing Date:</b> October 31, 2000
<b>TITLE OF INVENTION:</b> Very Long Instruction Word Microprocessor With Execution Packet Spanning Two or More Fetch Packets With Pre-Dispatch Instruction Selection From Two Latches According to Instruction Bit (amended title)	
<b>TI FILE NO.:</b> <b>TI-30559</b>	<b>DEPOSIT ACCT. NO.:</b> <b>20-0668</b>
<b>FAXED:</b> 2/27/06 <b>DUE:</b> 4/12/06 <b>ATTY/SECY:</b> RDM/reb	

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